



Fig. 2

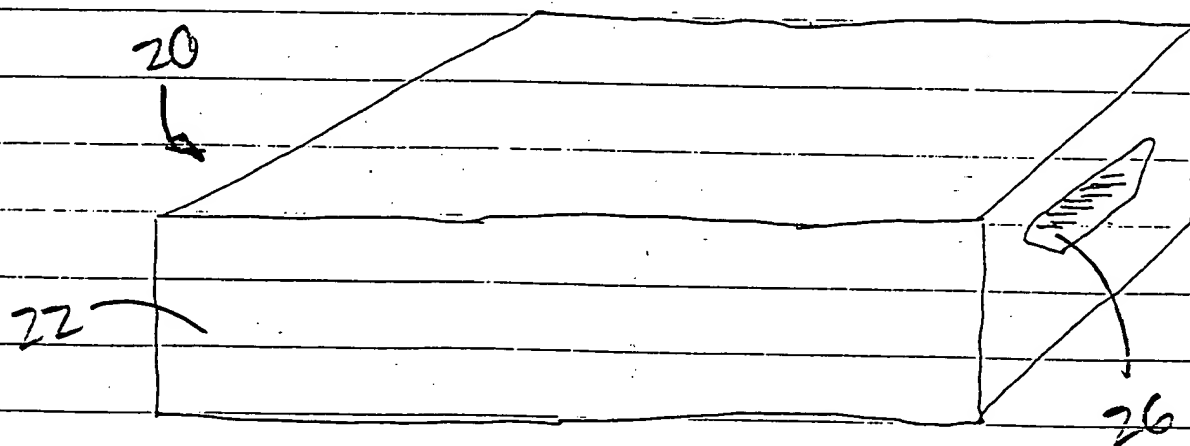
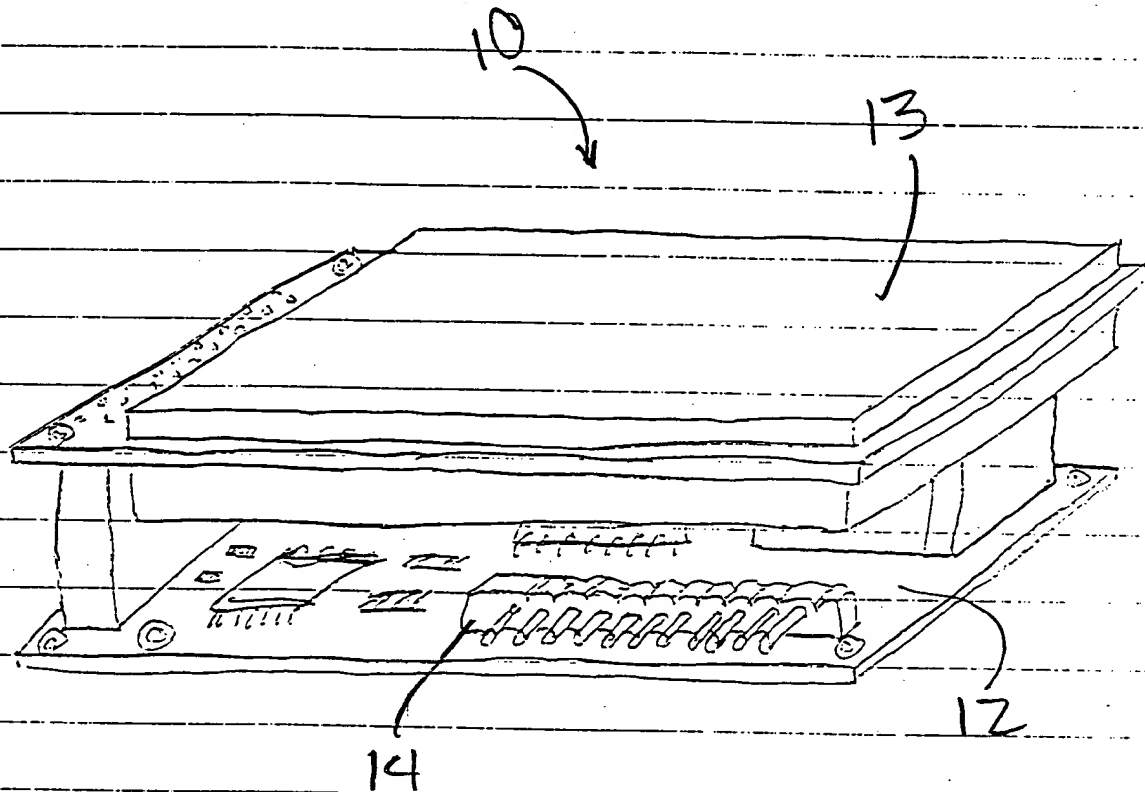
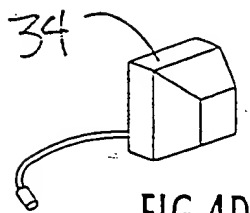
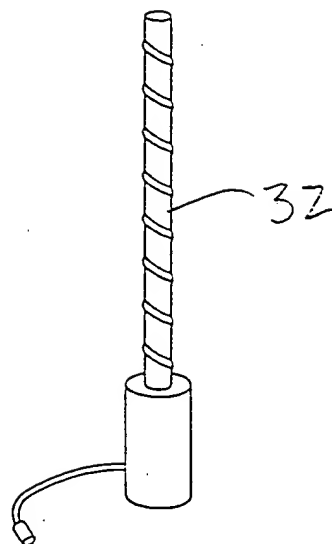
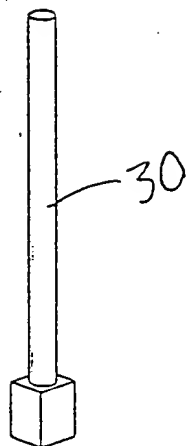
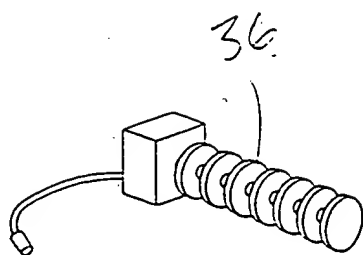


Fig. 3



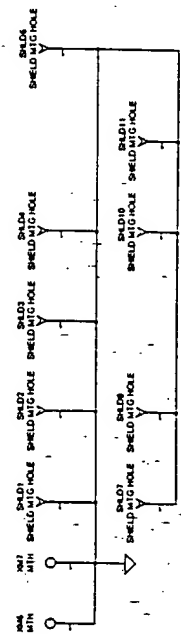
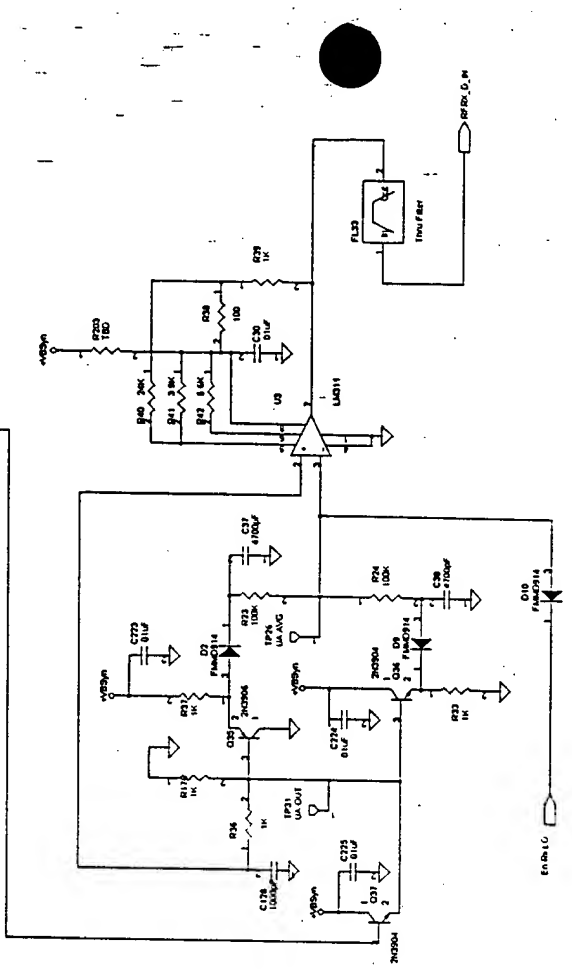
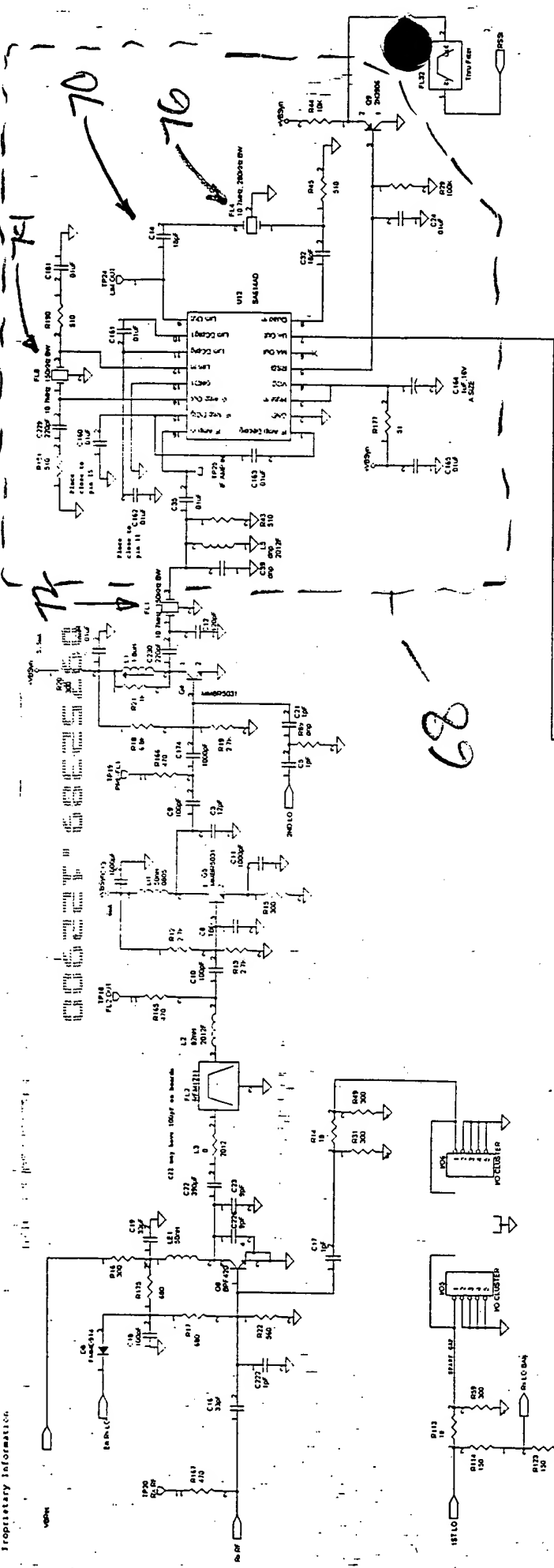


Fig. 5

**BLOCK DIAGRAM**

The block diagram illustrates the internal components of a radio receiver, including an IF AMP, a LIMITER, a SIGNAL STRENGTH indicator, a VOLTAGE REGULATOR, a MUTE indicator, and a QUAD DET. The circuit is powered by a VCC supply and grounded (GND). The diagram is annotated with handwritten numbers: '73' is written near the IF AMP, and '78' is written near the QUAD DET. A large arrow points from the top right towards the QUAD DET. The circuit is enclosed in a rectangular box with pins numbered 1 through 16 along the top and bottom edges.

Fig. 6a

**D and N Packages**

Pin	Function
1	IF AMP DECOUPLING
2	GND
3	MUTE INPUT
4	VCC
5	RSSI INPUT
6	MUTE AUDIO OUTPUT
7	UNMUTE AUDIO OUTPUT
8	QUADRATURE INPUT
9	LIMITER
10	LIMITER DECOUPLING
11	LIMITER INPUT
12	LIMITER
13	GND
14	IF AMP OUTPUT
15	IF AMP DECOUPLING
16	IF AMP INPUT

Fig 6b

Correct Offset

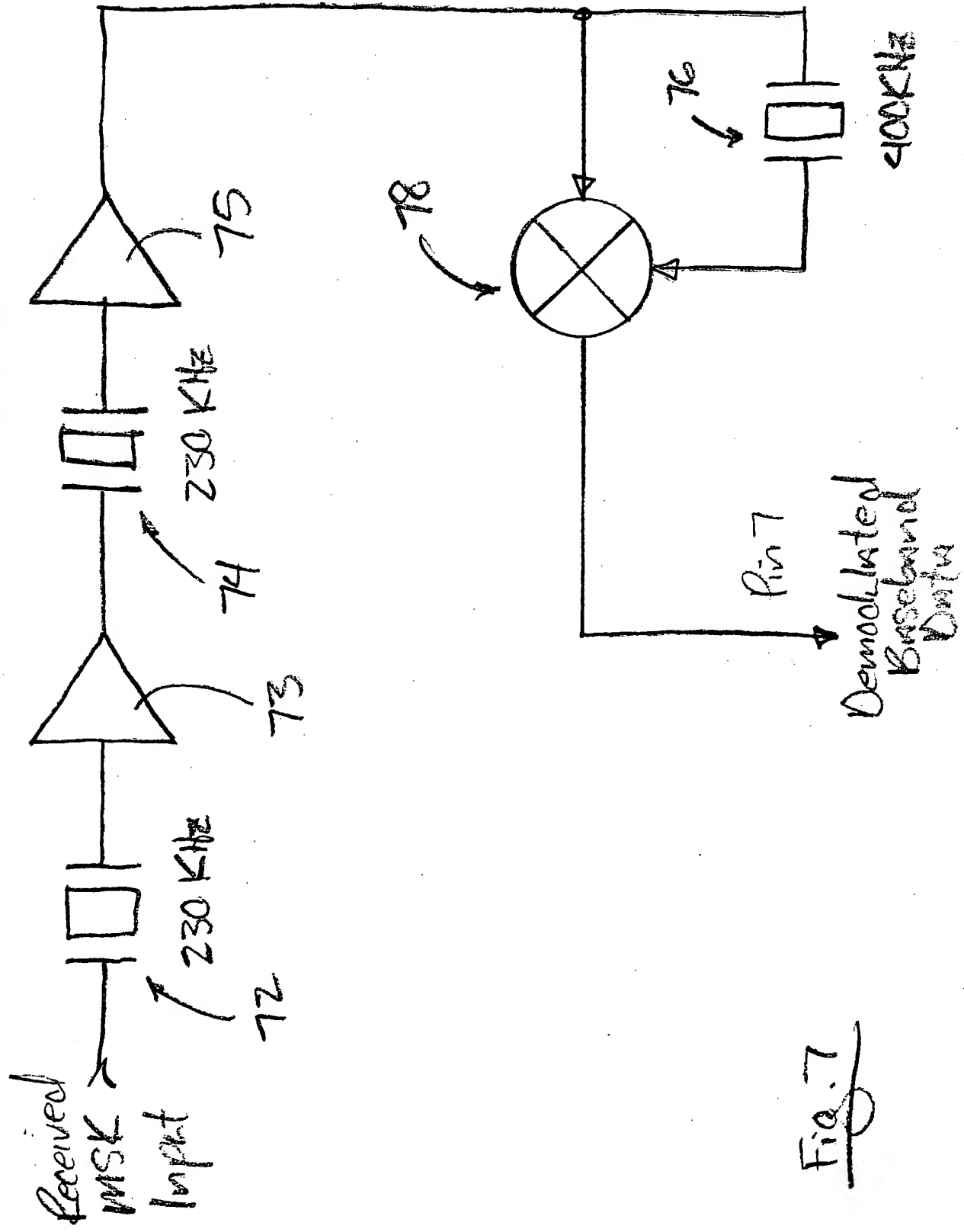


Fig. 7